

02-05-01

UTILITY PATENT APPLICATION TRANSMITTAL
(Only for new nonprovisional applications under 37 CFR 1.53(b))

10/20/01
Docket No.

Docket No. : 37115/JFO/B600
Inventor(s) : ESIN TERZIOGLU and MORTEZA CYRUS AFGHAHI

Title : ASYNCHRONOUSLY-RESETTABLE DECODER WITH REDUNDANCY

Express Mail Label No. : EL478475036US

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#4
Author/12/01
P. Lee 1/16/01
W.H.S.
1/16/01

ADDRESS TO: Assistant Commissioner for Patents

Box Patent Application
Washington, D.C. 20231

Date: February 2, 2001

1. FEE TRANSMITTAL FORM (Submit an original, and a duplicate for fee processing).

2. IF A CONTINUING APPLICATION

This application is a of patent application No. .

Prior application information: Examiner ; Group Art Unit:

This application claims priority pursuant to 35 U.S.C. §119(e) and 37 CFR §1.78(a)(4), to provisional Application Nos.

| | |
|------------|------------------|
| 60/179,777 | February 2, 2000 |
| 60/179,766 | February 2, 2000 |
| 60/179,866 | February 2, 2000 |
| 60/179,718 | February 2, 2000 |
| 60/179,765 | February 2, 2000 |
| 60/179,768 | February 2, 2000 |
| 60/179,865 | February 2, 2000 |
| 60/193,607 | March 31, 2000 |
| 60/193,606 | March 31, 2000 |
| 60/193,605 | March 31, 2000 |
| 60/215,741 | June 29, 2000 |
| 60/220,567 | June 25, 2000 |

The following related patent applications, assigned to the same assignee hereof and filed on even date herewith in the names of the same inventors as the present application, disclose related subject matter, with the subject of each being incorporated by reference herein in its entirety:

Memory Module with Hierarchical Functionality, Attorney Docket No. 40050/B600/JFO; High Precision Delay Measurement Circuit, Attorney Docket No. 37079/B600/JFO; Single-Ended Sense Amplifier with Sample-and-Hold Reference, Attorney Docket No. 37362/B600/JFO; Limited Switch Driver Circuit, Attorney Docket No. 37361/B600/JFO; Diffusion Replica Delay Circuit, Attorney Docket No. 37360/B600/JFO; Sense Amplifier with Offset Cancellation and Charge-Share Limited Swing Drivers, Attorney Docket No. 37363/B600/JFO; Memory Architecture with Single-Port Cell and Dual-Port (Read and Write) Functionality, Attorney Docket No. 37364/B600/JFO; Memory Redundancy Implementation, Attorney Docket No. 37496/B600/JFO; and; A Circuit Technique for High Speed Low Power Data Transfer Bus, Attorney Docket No. 37497/B600/JFO.

3. APPLICATION COMPRISED OF

Specification

Specification, claims and Abstract (total pages)

UTILITY PATENT APPLICATION TRANSMITTAL
(Only for new nonprovisional applications under 37 CFR 1.53(b))

Docket No.: 37115/JFO/B600

Drawings

22 Sheets of drawing(s) (FIGS. 1 to 22B)

Declaration and Power of Attorney

Newly executed

Unexecuted declaration

Copy from a prior application (37 CFR 1.63(d))(for continuation and divisional)

4. **Microfiche Computer Program (Appendix)**

5. **Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary)**

Computer Readable Copy

Paper Copy (identical to computer copy)

Statement verifying identity of above copies

6. **APPLICANT(S) STATUS UNDER 37 CFR § 1.27**

NO Applicant(s) and any others associated with it/them under § 1.27(a) are a SMALL ENTITY

7. **ALSO ENCLOSED ARE**

Preliminary Amendment

A Petition for Extension of Time for the parent application and the required fee are enclosed as separate papers

An Assignment of the invention with the Recordation Cover Sheet and the recordation fee are enclosed as separate papers

This application is owned by pursuant to an Assignment recorded at Reel , Frame Information Disclosure Statement (IDS)/PTO-1449

Copies of IDS Citations

Certified copy of Priority Document(s) (if foreign priority is claimed)

English Translation Document (if applicable)

Return Receipt Postcard (MPEP 503) (should be specifically itemized).

Other

8. **CORRESPONDENCE ADDRESS**

CHRISTIE, PARKER & HALE, LLP, P.O. BOX 7068, PASADENA, CA 91109-7068

Customer Number: 23363

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

By

John F. O'Rourke
Reg. No. 38,985
626/795-9900

JFO/vat

**FEE TRANSMITTAL
UTILITY PATENT APPLICATION**

DATE: February 2, 2001

Docket No. : 37115/JFO/B600

Inventor(s) : MORTEZA CYRUS AFGHAHI and ESIN TERZIOGLU

Title : HIGH PRECISION DELAY MEASUREMENT CIRCUIT

FEE DETERMINATION

| CLAIMS AS FILED | | | | | |
|-------------------------------|--------------|--------------|-------------------|-------------------|----------|
| | NUMBER FILED | NUMBER EXTRA | SMALL ENTITY RATE | LARGE ENTITY RATE | Fee |
| TOTAL CLAIMS | 17 - 20 | 0 | x \$9.00 | x \$18.00 | \$0.00 |
| INDEPENDENT CLAIMS | 3 - 3 | 0 | x \$40.00 | x \$80.00 | \$0.00 |
| MULTIPLE-DEPENDENT CLAIMS FEE | | | \$135.00 | \$270.00 | |
| BASIC FEE | | | \$355.00 | \$710.00 | \$710.00 |
| TOTAL FILING FEE | | | | | \$710.00 |

List Independent Claims: 1, 6, 12

METHOD OF PAYMENT

Payment Enclosed: Check for \$710.00

The Commissioner is hereby authorized to charge any fees under 37 CFR 1.16 and 1.17 which may be required during the **entire pendency** of the application to Deposit Account No. 03-1728. Please show our docket number with any charge or credit to our Deposit Account. A **duplicate copy of this sheet is enclosed**.

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

By _____

John F. O'Rourke
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JFO/vat

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